A VARINDUSTRIES

QUESTA LINT

Automated verification for high-quality, clean RTL design

Questa Lint delivers fast and automated verification at the design stage, ensuring that your RTL is clean and free from errors without waiting for a testbench. It identifies issues related to completeness, consistency, and quality using syntactic, semantic, stylistic, and structural analyses. This early-stage verification saves time and prevents costly fixes later in the development process.

Out-of-the-box methodologies and pre-built checksets for industry standards, such as ISO 26262, DO-254, and STARC, enable immediate productivity and continuous design quality improvement. Questa Lint's deep learning and data analysis capabilities report only the issues that matter, providing adaptive insights into your design's quality metrics and trends over time.

Why Choose Questa Lint?

Fast RTL analysis without testbenches

Questa Lint enables quick verification of your RTL design, detecting issues at the earliest stage, before simulation or testbench creation, saving significant time in identifying completeness and consistency issues.

Pre-configured methodologies and standards

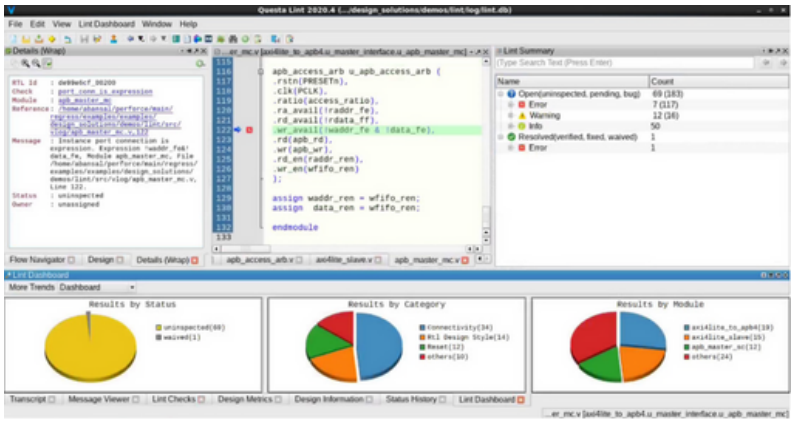
With out-of-the-box support for IP, SoC, and FPGA development, Questa Lint boosts team productivity from day one. It also includes pre-built checksets for industry standards like ISO 26262 and DO-254, along with customizable methodologies for your specific design needs.

Deep learning-driven insights

Questa Lint applies deep learning to analyze known issues and report only the ones that deviate from inferred design intent. It provides scores for design quality, helping teams focus on areas that may require attention before they become critical.

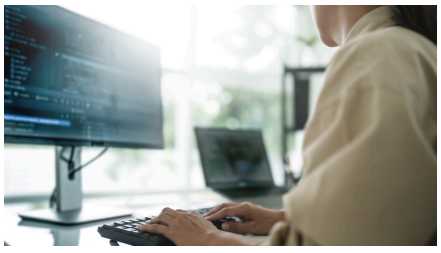
Continuous integration for ongoing cleanliness

Integrated into popular Continuous Integration (CI) tools, Questa Lint ensures that once a design is proven clean, it stays that way throughout the development process, maintaining high-quality design standards.



Using only your RTL, Questa Lint provides intent and quality-focused insights from the first line of code. Metrics and quality scores are tracked over time with trend analysis to ensure the design stays clean—no testbench required.

Key Features of Questa Lint



Fast RTL Analysis

Questa Lint quickly analyzes your RTL design for completeness, consistency, and quality issues without the need for a testbench. This enables rapid identification of problems at the earliest stage of development, reducing the risk of costly rework later in the process.

Out-of-the-Box Methodologies

With pre-configured methodologies tailored for IP, SoC, and FPGA development, Questa Lint enhances team productivity from the start. Pre-built checksets for industry standards, such as ISO 26262, DO-254, and STARC, help ensure compliance, while flexible configuration options allow teams to adapt checks to their unique design goals.

Adaptive Design Insights

Questa Lint leverages advanced data analysis and deep learning techniques to report only the most critical issues, filtering out less relevant warnings. It scores design quality, enabling teams to focus on potential problem areas and optimize their designs for long-term reliability.

Continuous Integration Support

Questa Lint integrates seamlessly with popular Continuous Integration (CI) systems, ensuring that design quality is continuously maintained. As development progresses, Questa Lint ensures that any new issues are detected immediately, allowing for quick fixes and preventing regression.

Minimized Gate-Level Simulation

With Questa Lint's design quality scoring, teams can monitor the cleanliness of their design over time. These metrics, along with trending data, provide valuable insights into the state of the design and help maintain consistently high-quality RTL throughout the development cycle.