QUESTA BASE

Sophisticated FPGA verification

Questa™ Base packs unprecedented verification capabilities in a cost-effective HDL simulation solution for complex digital designs. It enables faster identification of design issues, ensuring thorough verification across multiple design environments, helping you achieve higher quality and reliability.

By offering a unified verification environment, Questa Base reduces time-to-market and improves the overall design process. Its advanced simulation and debugging capabilities allow for early detection of potential problems, ultimately improving productivity and product performance.

Why choose Questa Base for simulation?

Comprehensive functional verification

Questa Base delivers advanced simulation and verification capabilities, supporting a wide range of digital designs. This ensures thorough coverage and validation, reducing the risk of design flaws and improving product reliability.

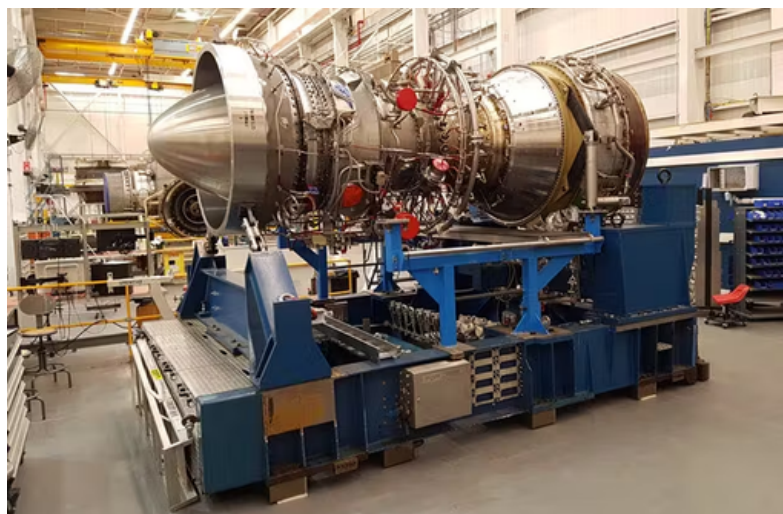### Unified verification environment

Questa Base provides a single, integrated environment for design verification, simplifying workflows and reducing complexity. Teams can collaborate more effectively with consistent tools, reducing errors and streamlining the development process.

Advanced debugging tools

Questa Base offers powerful debugging features that allow for rapid identification and resolution of design issues. With real-time feedback and analysis, you can quickly pinpoint problems and make necessary adjustments, minimizing delays.

Faster time-to-market

Questa Base optimizes the design verification process, reducing the time required to detect and fix issues. This accelerates the overall development cycle, helping you bring products to market faster while maintaining high quality.

The comprehensive advanced verification platform of Questa advanced simulator
is capable of reducing the risk of validating complex FPGA and SoC designs.

Comprehensive SoC Verification

Questa Base delivers advanced simulation technology designed to tackle the growing complexity of SoC designs. It supports multiple embedded processors and advanced interconnect systems, making it ideal for large, multi-platform projects. By unifying a broad range of verification features, it ensures seamless coverage across all design environments.

Unified Debug and Optimization

Questa Base offers a high-performance debug environment, combining code coverage, functional coverage, and automated verification processes. The platform accelerates the identification of potential issues, improving debug productivity and ensuring that the system functions as expected.

Multi-Language Support and Compliance

Questa Base supports multiple languages, including UVM, SystemVerilog, VHDL, SystemC, and mixed-language environments, ensuring broad compatibility and easing the integration with different design flows. As an industry leader, it guarantees compliance with the latest standards, enhancing reliability and efficiency.

Cutting-Edge Features for Design Performance

With its support for power-aware verification, X-propagation handling, and real number modeling, Questa Base provides unparalleled insight into design performance. Its integration with Siemens' Questa Verification IQ also offers detailed trend analysis and test management for better risk management and compliance tracking.

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